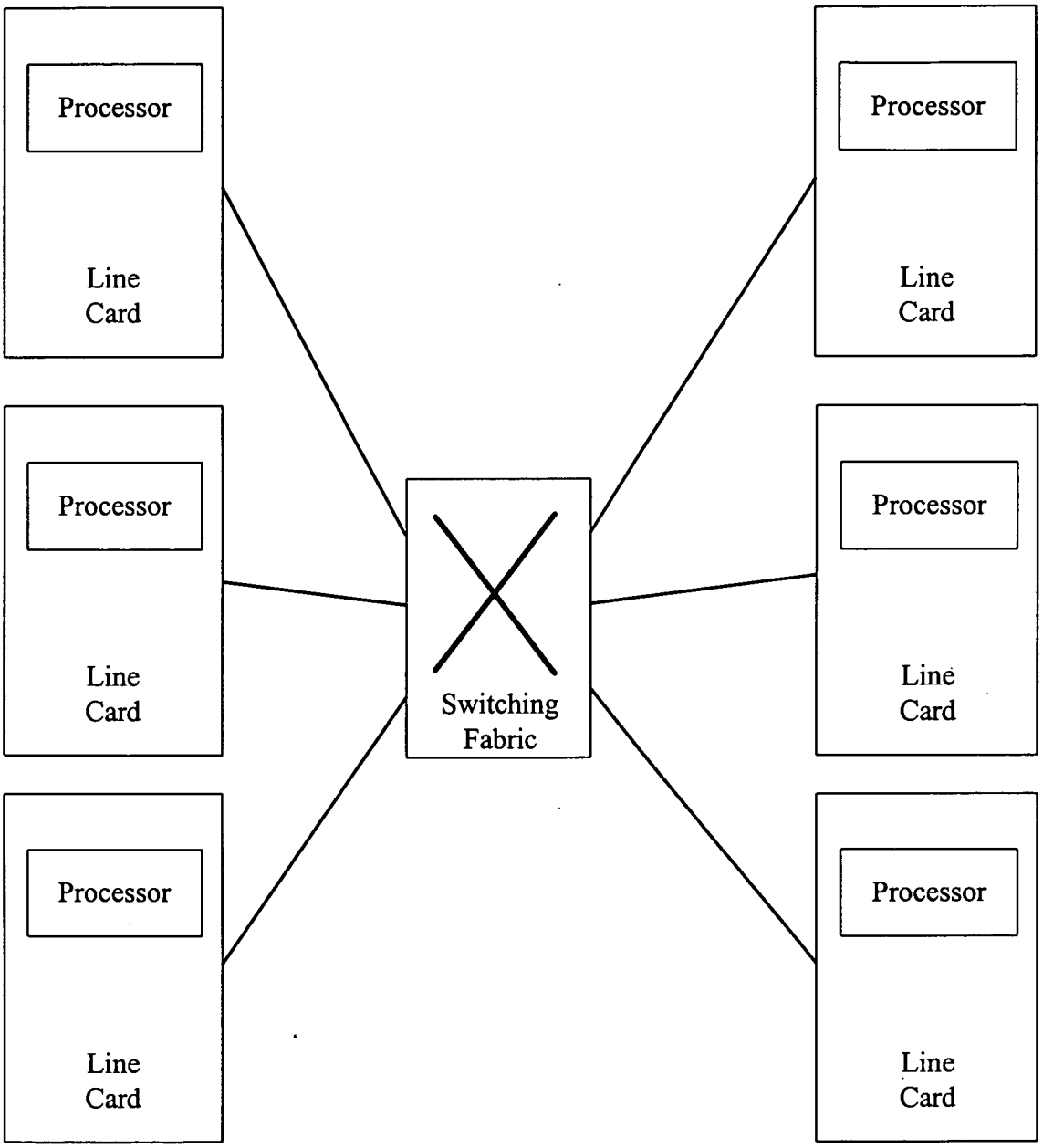

Figure 1.
(PRIOR ART)

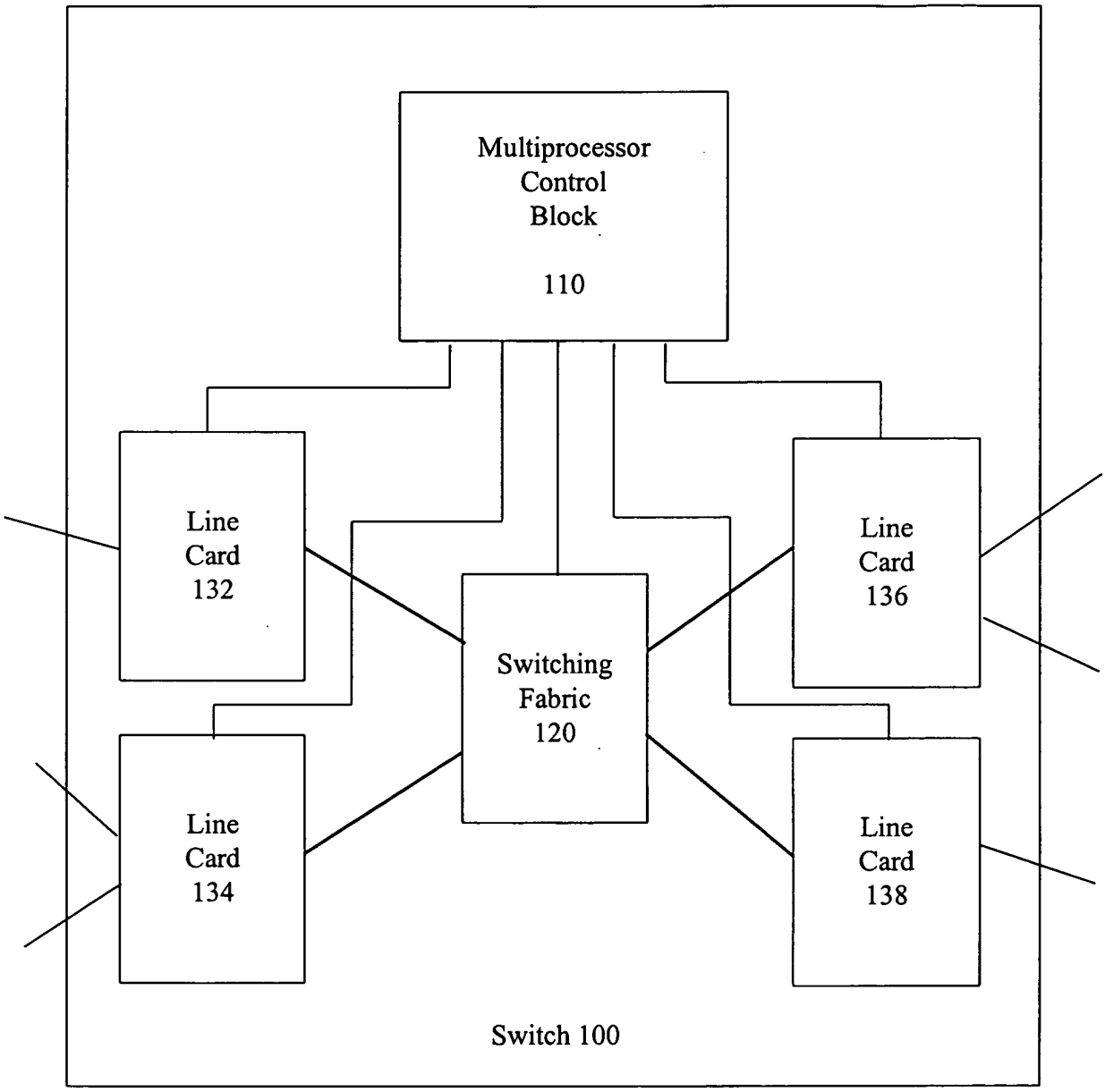
Multiprocessor
Control
Block
110
Line
Card
132
Switching
Fabric
120
Line
Card
136
Line
Card
134
Line
Card
138
Switch 100

Figure 2.

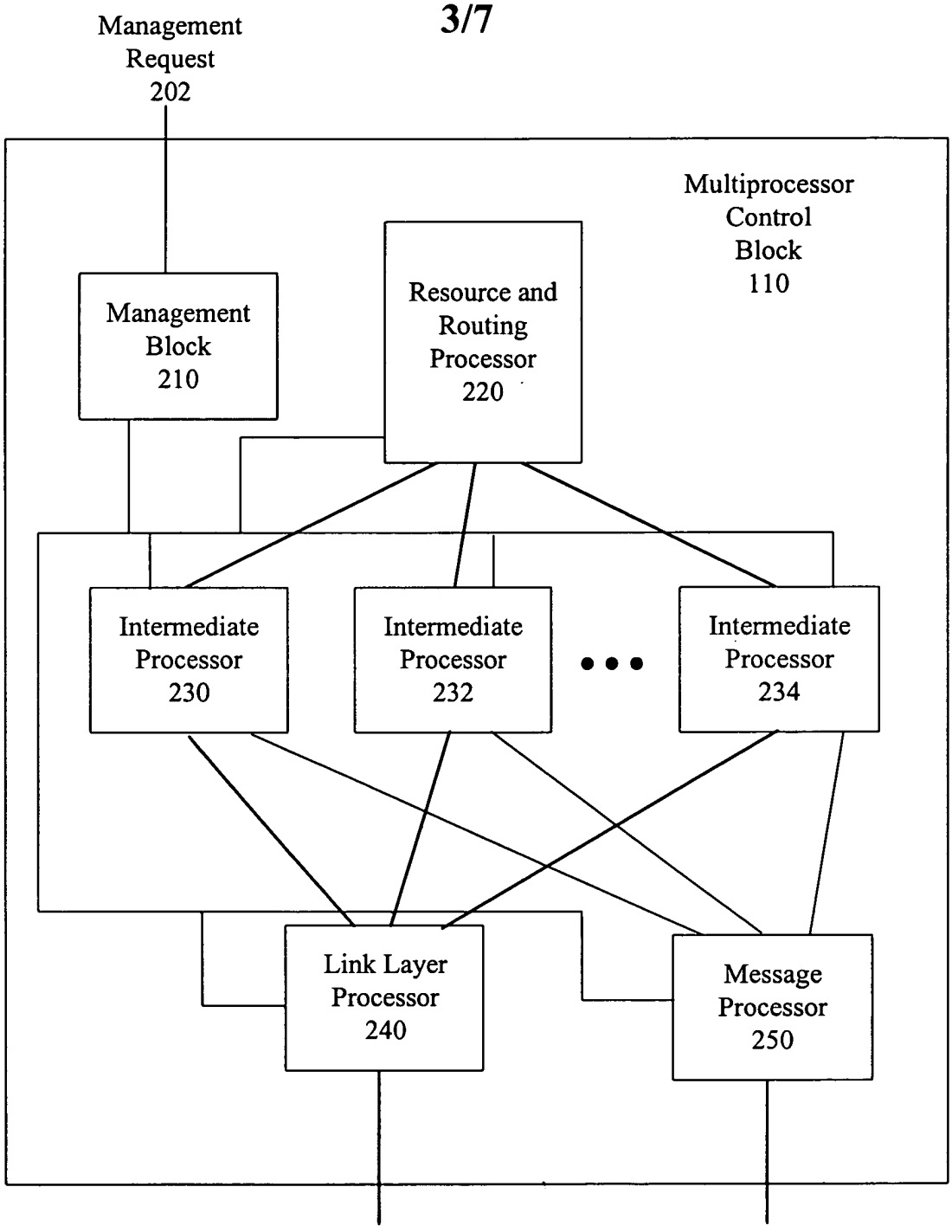
Management
Request
202
Multiprocessor
Control
Block
110
Management
Block
210
Resource and
Routing
Processor
220
Intermediate
Processor
230
Intermediate
Processor
232
Intermediate
Processor
234
Link Layer
Processor
240
Message
Processor
250

Figure 3.

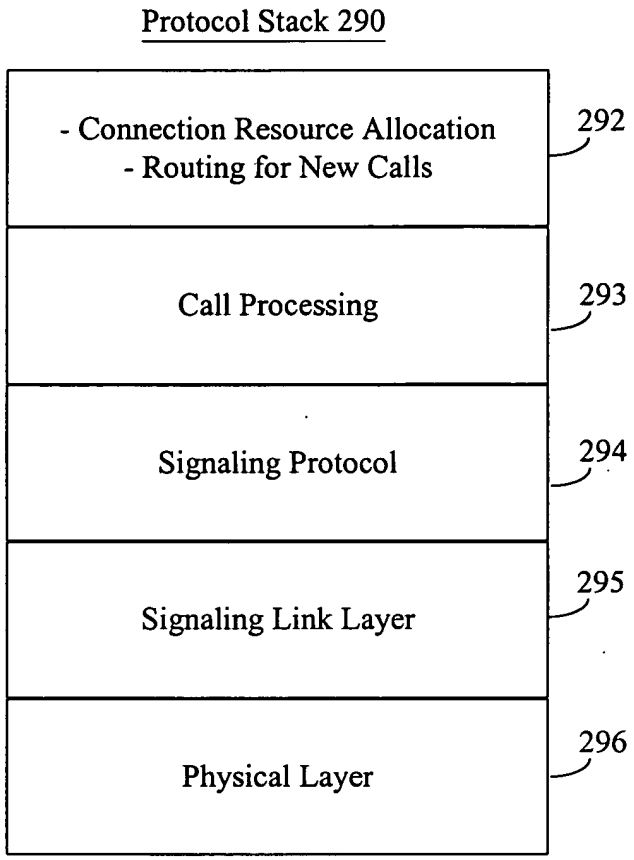
Protocol Stack 290
- Connection Resource Allocation
- Routing for New Calls
292
Call Processing
293
Signaling Protocol
294
Signaling Link Layer
295
Physical Layer
296

Figure 4.

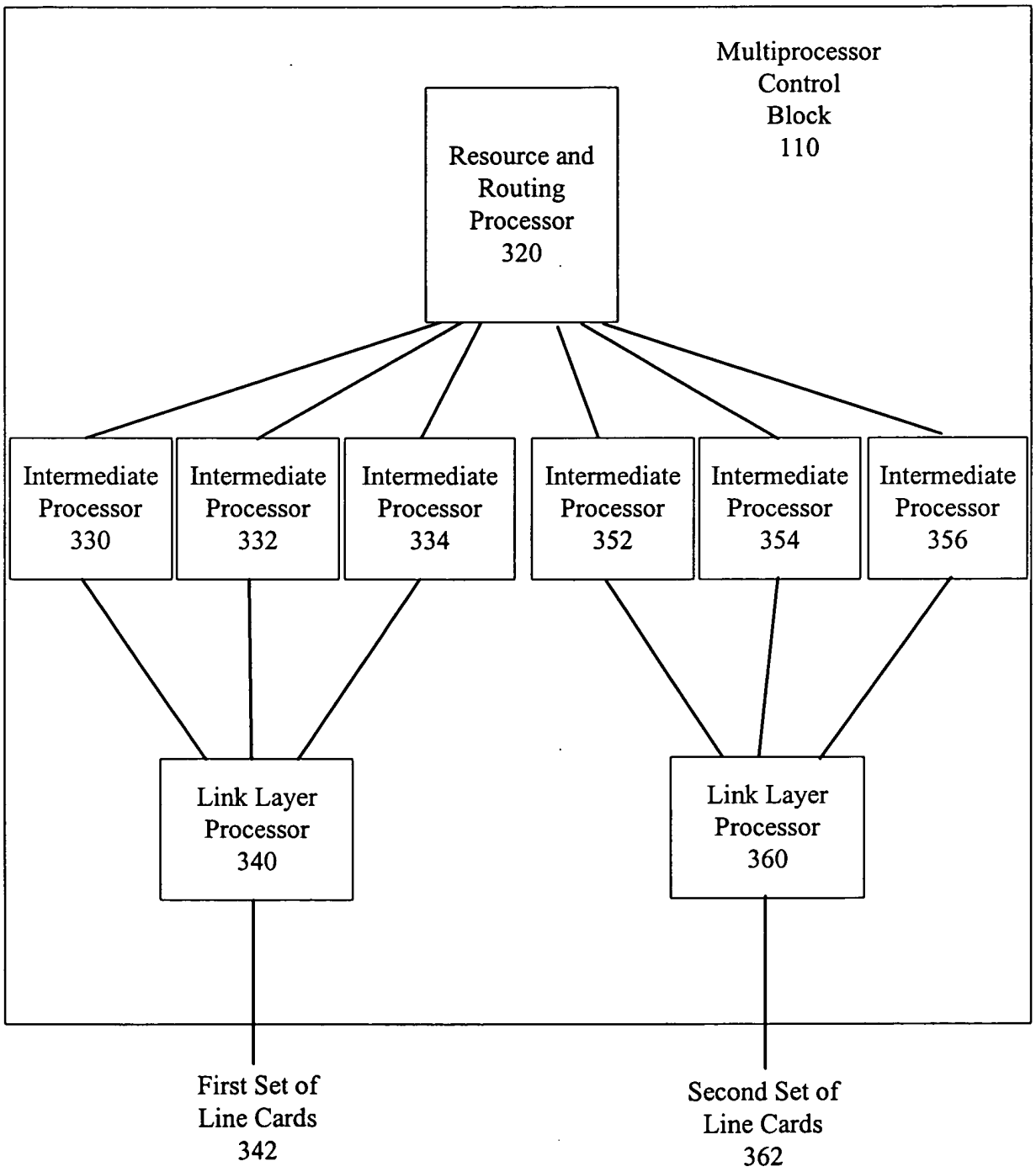
Multiprocessor Control Block 110
Resource and Routing Processor 320
Intermediate Processor 330
Intermediate Processor 332
Intermediate Processor 334
Intermediate Processor 352
Intermediate Processor 354
Intermediate Processor 356
Link Layer Processor 340
Link Layer Processor 360
First Set of Line Cards 342
Second Set of Line Cards 362

Figure 5.

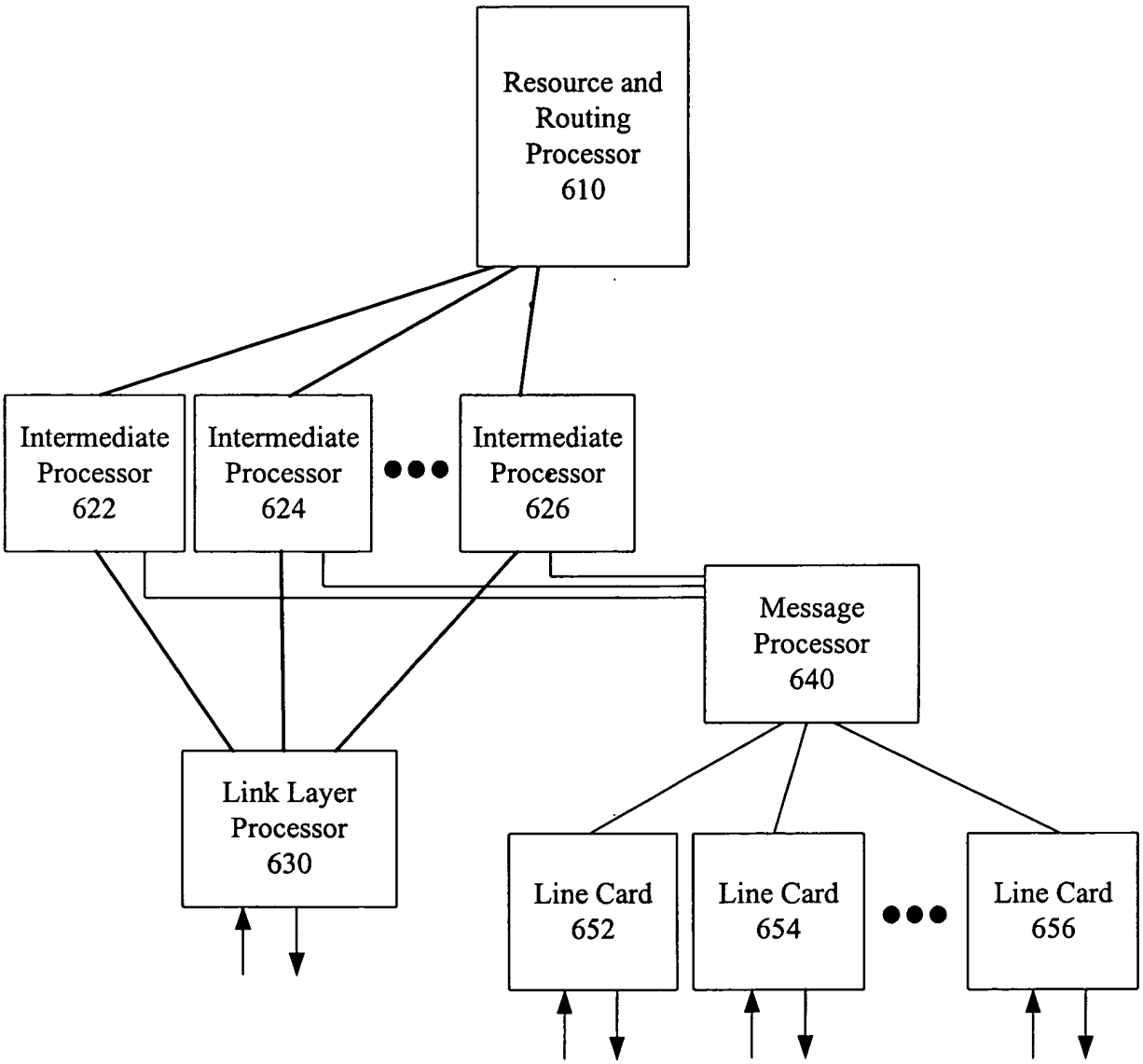
Resource and Routing Processor 610
Intermediate Processor 622
Intermediate Processor 624
Intermediate Processor 626
Message Processor 640
Link Layer Processor 630
Line Card 652
Line Card 654
Line Card 656

Figure 6.

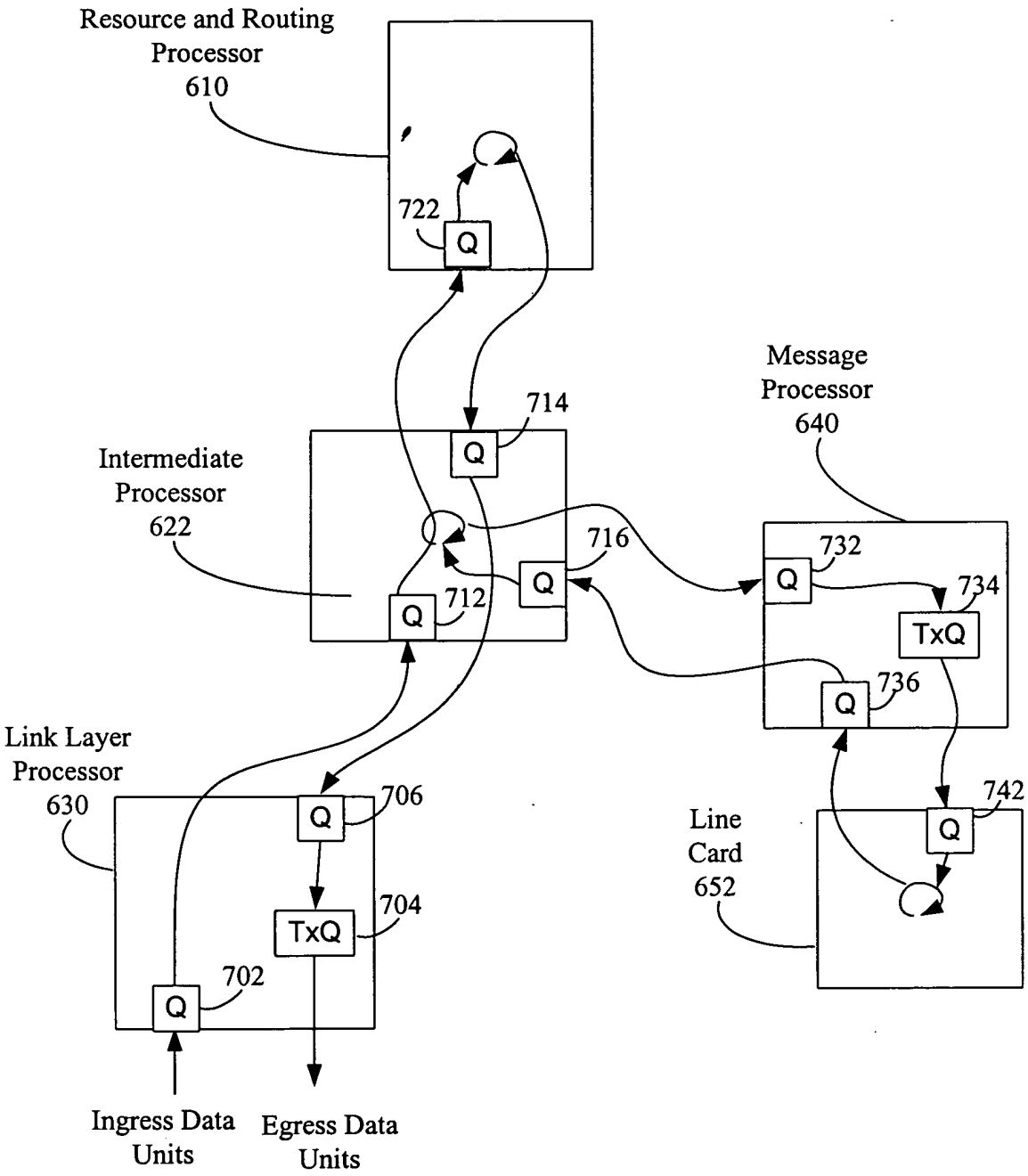
Resource and Routing Processor 610
722
Q
714
Intermediate Processor 622
716
712
Message Processor 640
732
734
TxQ
736
Link Layer Processor 630
706
704
702
Line Card 652
742
Ingress Data Units
Egress Data Units

Figure 7.